

Please type a plus sign (+) inside this box →

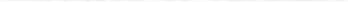
PTO/SB/06A (10-96)

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<p>Substitute for form 1449A/PTO</p> <p>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</p>				<p><i>Completeness</i></p>												
Sheet	1	of	2													
				<table border="1"> <tr> <td>Application Number</td> <td>09/469,406</td> </tr> <tr> <td>Filing Date</td> <td>December 22, 1999</td> </tr> <tr> <td>First Named Inventor</td> <td>Ali Keshavarzi</td> </tr> <tr> <td>Group Art Unit</td> <td>2811</td> </tr> <tr> <td>Examiner Name</td> <td>Not Yet Assigned</td> </tr> <tr> <td>Attorney Docket Number</td> <td>042390.P7511</td> </tr> </table>	Application Number	09/469,406	Filing Date	December 22, 1999	First Named Inventor	Ali Keshavarzi	Group Art Unit	2811	Examiner Name	Not Yet Assigned	Attorney Docket Number	042390.P7511
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U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Signature		Date Considered	10-30-00
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Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication

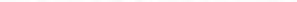
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OTHER ART (Including Title, Author, Date, Pertinent Pages, etc.)

Exam. Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
WHL	6.	N. Weste et al., "Principles of CMOS VLSI Design" (Addison-Wesley, 2nd Ed. 1993), pp. 41-51 and 180-87.
WHL	7.	R. Pierret, "Field Effect Devices, Vol. IV of Modular Series on Solid State Devices" (Addison-Wesley, 2nd Ed. 1990), pp. 47-59.

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